

## SED1026

### 60V,300mA N-Channel MOSFET

Revision:A

**General Description**

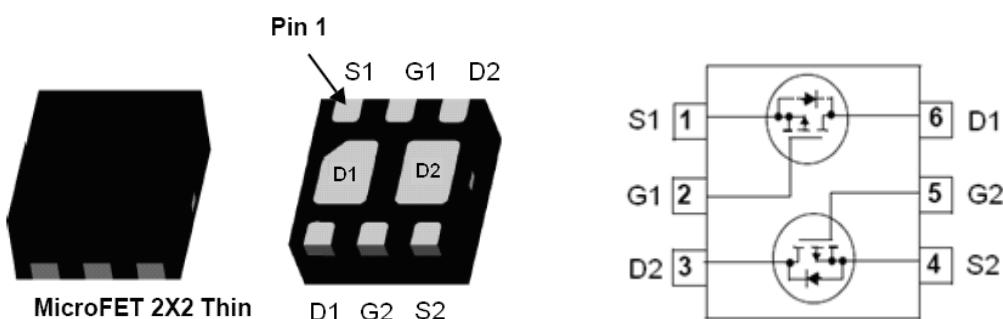
The MOSFETs from SINO-IC provide the best combination of fast switching, low on-resistance and cost-effectiveness.

**Features**

- $V_{DS(V)} = 60V$
- $I_D = 300mA$
- $R_{DS(ON)} < 2\Omega$  ( $V_{GS} = 10V, I_D=0.5A$ )
- $R_{DS(ON)} < 3\Omega$  ( $V_{GS} = 5V, I_D=0.05A$ )

**Pin configurations**

See Diagram below

**Absolute Maximum Ratings**

Parameter		Symbol	Rating	Units
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Drain Current (Note 1)	Continuous	$I_D$	300	mA
	Pulsed		800	
Total Power Dissipation		$P_D$	350	mW
Operating Junction Temperature Range		$T_J$	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A $t \leq 5s$	$R_{JA}$	357	-	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>OFF/ON CHARACTERISTICS (Note 2)</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$I_D=10 \mu\text{A}, V_{GS}=0 \text{V}$	60			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=60 \text{V}, V_{GS}=0 \text{V}$			1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body leakage current	$V_{DS}=0 \text{V}, V_{GS}=\pm 20 \text{V}$			$\pm 10$	$\mu\text{A}$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	0.45		0.85	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance2	$V_{GS}=10 \text{V}, I_D=0.5 \text{A}$		2	-	$\Omega$
		$V_{GS}=5 \text{V}, I_D=0.05 \text{A}$		3	-	$\Omega$
$ Y_{fs} $	Forward Transfer Admittance	$V_{GS} = 10 \text{V}, I_S = 0.2 \text{A}$	80			ms
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0 \text{V}, V_{DS}=25 \text{V}, f=1 \text{MHz}$			50	pF
$C_{oss}$	Output Capacitance				25	pF
$C_{rss}$	Reverse Transfer Capacitance				5	pF

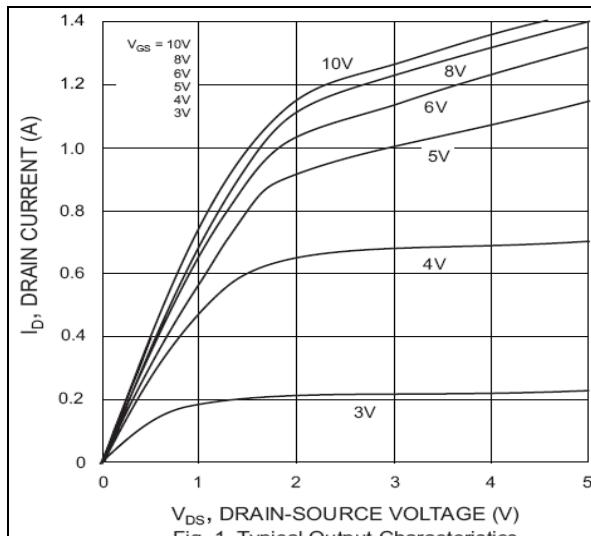
**Typical Characteristics**

Fig. 1 Typical Output Characteristics

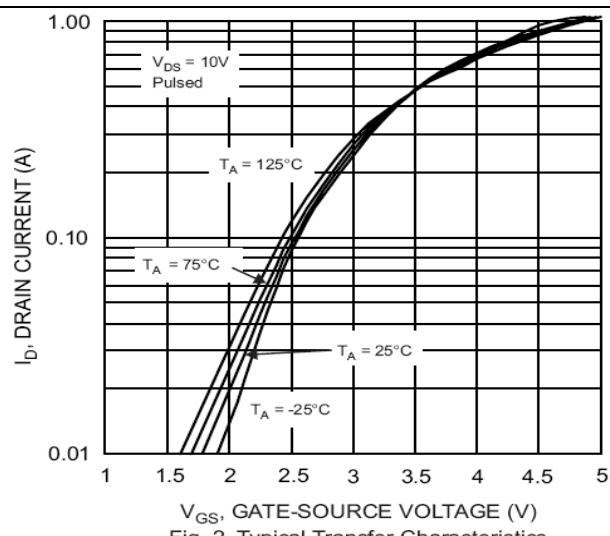


Fig. 2 Typical Transfer Characteristics

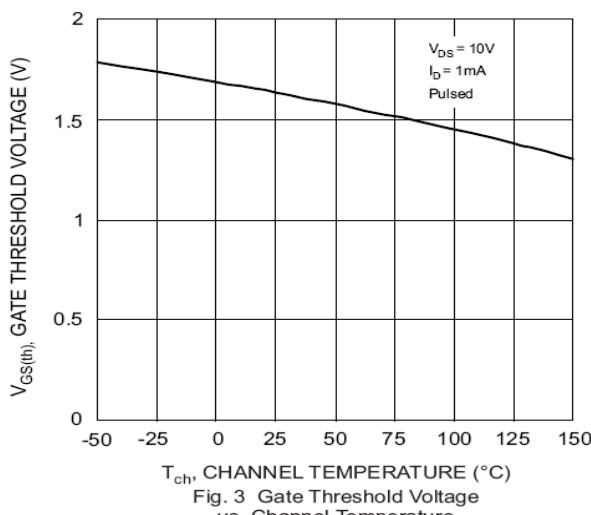


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

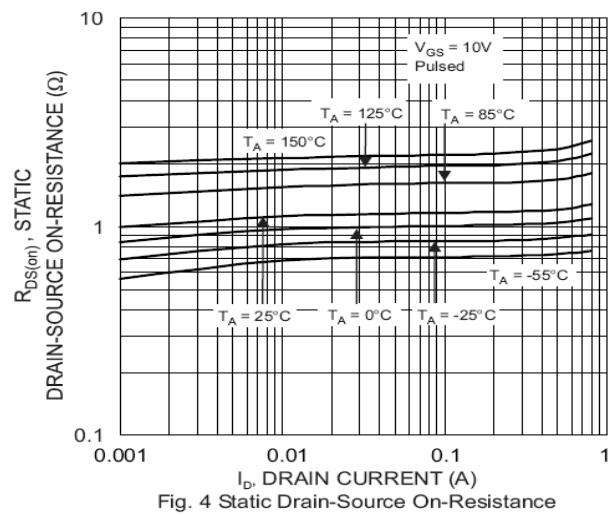


Fig. 4 Static Drain-Source On-Resistance Vs. Drain Current

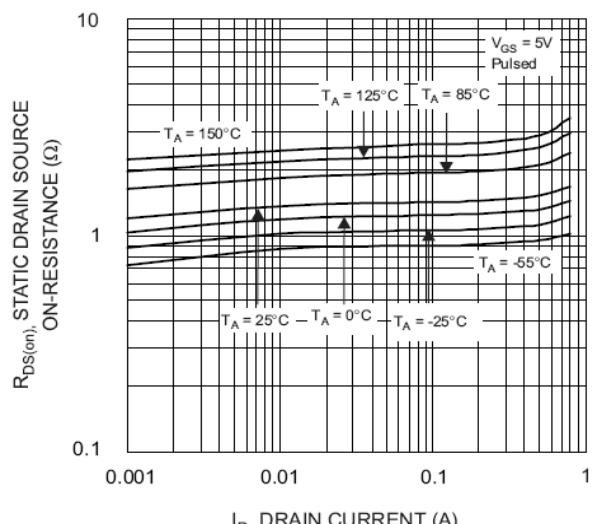


Fig. 5 Static Drain-Source On-Resistance  
vs. Drain Current

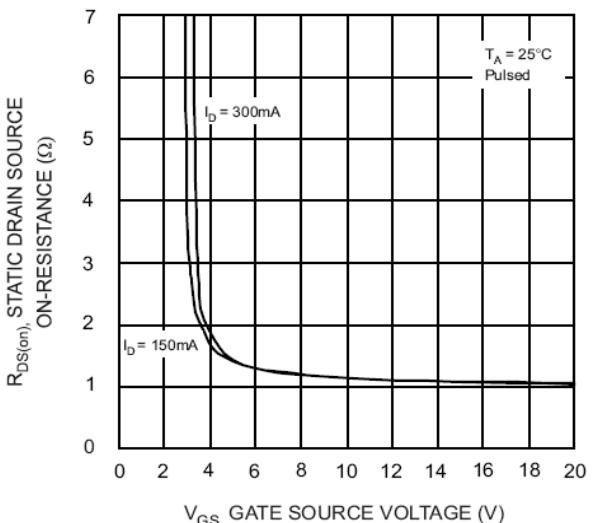


Fig. 6 Static Drain-Source On-Resistance  
vs. Gate-Source Voltage

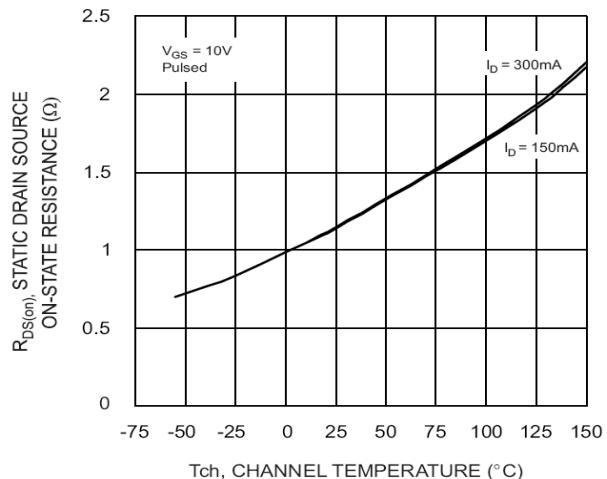


Fig. 7 Static Drain-Source On-State Resistance  
vs. Channel Temperature

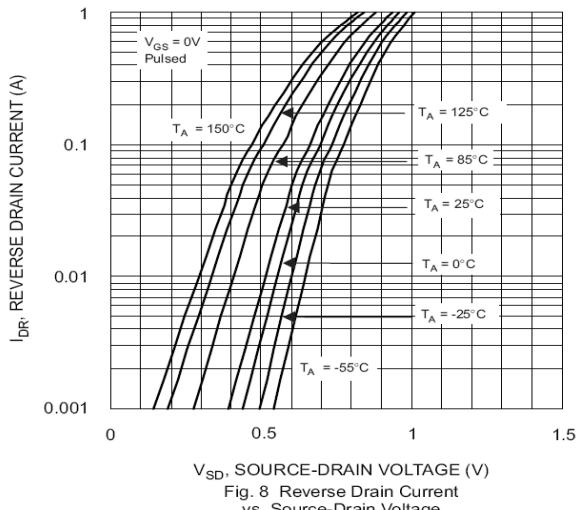


Fig. 8 Reverse Drain Current  
vs. Source-Drain Voltage

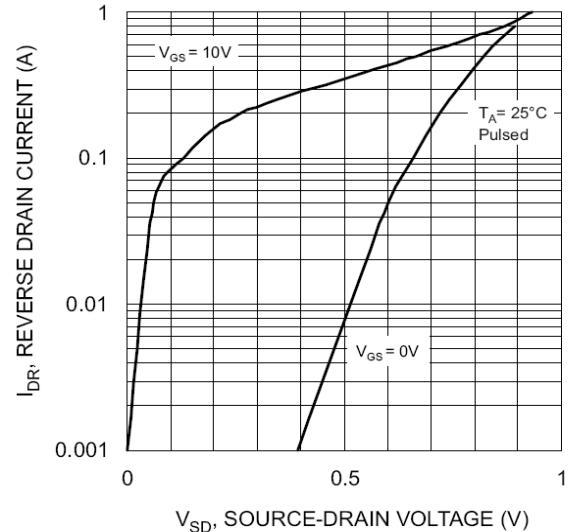


Fig. 9 Reverse Drain Current  
vs. Source-Drain Voltage

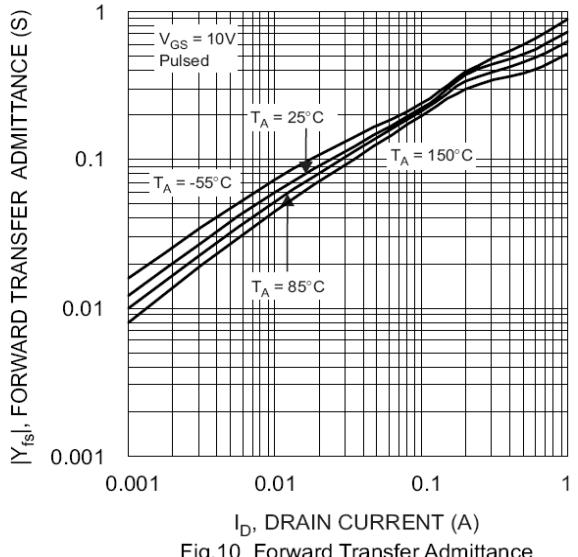
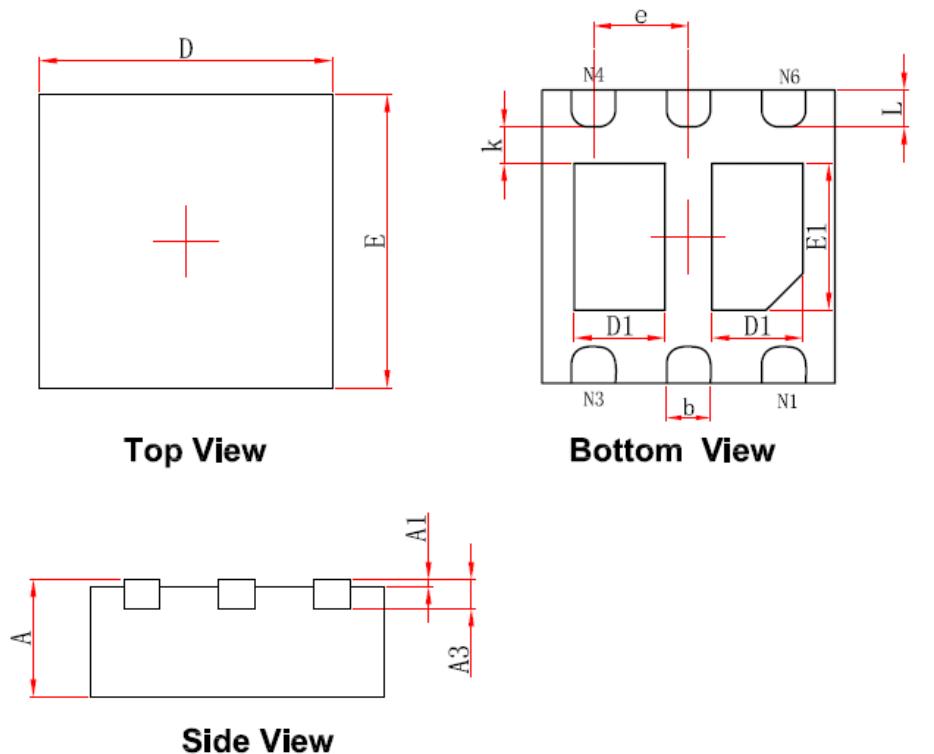


Fig. 10 Forward Transfer Admittance  
vs. Drain Current

## DFNWB2×2-6L-A (P0.65T0.75/0.85) PACKAGE OUTLINE DIMENSIONS



<b>Symbol</b>	<b>Dimensions In Millimeters</b>		<b>Dimensions In Inches</b>	
	<b>Min.</b>	<b>Max.</b>	<b>Min.</b>	<b>Max.</b>
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.924	2.076	0.076	0.082
E	1.924	2.076	0.076	0.082
D1	0.520	0.720	0.020	0.028
E1	0.900	1.100	0.035	0.043
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
L	0.174	0.326	0.007	0.013

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